

[54] **HIGH SPEED, LOW POWER CMOS D/A CONVERTER FOR WAVE SYNTHESIS IN NETWORK**

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[57] **ABSTRACT**

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A high speed low power digital-to-analog (D/A) converter (DAC) includes a plurality of least significant bit (LSB) cells that collectively define a total output of the DAC. Each LSB cell includes a differential current driver that has reduced capacitive loading due to a cascode structure of the current driver wherein transistors are biased to desired levels and current sources are switched on and off to control the differential output signal.

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[52] **U.S. Cl.** **341/144; 341/135**

[58] **Field of Search** 341/144, 136, 341/153, 154; 323/316

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12 Claims, 3 Drawing Sheets

